#10/13

T-565 P.003/011 F-438

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ONS00317 10/072,145

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

7/21/03

In re Application of:

Averett, Guy E. et al.

FAX RECEIVED

Serial No.:

10/072,145

JUL 1 6 2003

Filing Date:

02/07/2002

TECHNOLOGY CENTER 2800

Group Art Unit:

2811

2011

Examiner:

Title:

Magee, Thomas J.

SEMICONDUCTOR DEVICE AND METHOD OF

PROVIDING REGIONS OF LOW SUBSTRATE

CAPACITANCE

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING FACSIMILE TRANSMITTED TO THE PATENT AND TRADEMARK OFFICE, ATTN: EXAMINER Thomas J. Magee AT THE FAX NO. (703) 872-9318

SIGNATURE SIGNATURE

DATE

Honorable Commissioner of Patents, P.O. BOX 1450 Alexandria, VA 22313-1450

AMENDMENT

SIR:

In response to the office action mailed March 28, 2003, please amend the above-identified application as follows.

07/21/2003 PBRITTON 00000007 501086 10072145

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110.00 DA